## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

James Vernon Rhodes, et al.

Serial No.: 10/759,910

Filed: January 16, 2004

Docket No.: 550,702

Group Art Unit: 2829

Examiner: Minh N. Tang

Date: April 25, 2005

FOR:

ELECTROWECHANICAL MODULE, FOR HOLDING IC-CHIPS IN A CHIP TESTING SYSTEM, THAT SYNCHRONIZES AND TRANSLATES TEST SIGNALS TO THE IC-CHIPS

## AMENDMENT

Mail Stop AMENDMENT Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

This amendment is in response to the Office Action that was mailed on March 31, 2005.